



2829

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Seetharaman Sridhar, et al.

Docket No: TI-31408

Serial No: 10/005,785

Conf. No: 1353

Examiner: Jermele M. Hollington

Art Unit: 2829

Filed: 11/08/2001

For: PROCESS TO IMPROVE NWell-NWell ISOLATION WITH A BLANKET LOW DOSE  
HIGH ENERGY IMPLANT

**ELECTION**

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)**

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 10-20-03.

  
Ann Trent

Dear Sir:

This election is offered in response to the Examiner's restriction requirement mailed September 26, 2003.

Applicants hereby elect to pursue Group I of Claims 1-2, drawn to integrated circuit structure, without traversing the Examiner's restriction requirement. Applicant further elects species I drawn to claim 1 without traversing the Examiner's restriction.

Respectfully submitted,



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